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| EFS ID: | 1411091 | | | |
| Application Number: | 10072145 | | | |
| International Application Number: | | | | |
| Confirmation Number: | 1448 | | | |
| Title of Invention: | Semiconductor device and method of providing regions of low substrate capacitance | | | |
| First Named Inventor/Applicant Name: | Guy E. Averett | | | |
| Correspondence Address: | ON Semiconductor Patent Administration Dept - MD A700 P.O. Box 62890 - Phoenix AZ 85082-2890 US 6022443169 | | | |
| Filer: | Kevin Brian Jackson/Becky Newnam | | | |
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File Listing:

| Document Number | Document Description | File Name | File Size(Bytes) | Multi Part /.zip | Pages (if appl.) |
|--------------------|--|----------------------------|------------------|---------------------|---------------------|
| 1 | Amendment - After Non-Final Rejection | ONS00317AMENDMENTB.p df | 137376 | no | 5 |
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New Applications Under 35 U.S.C. 111

Information:

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.